

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **IIDA, Kenji**

Group Art Unit: **2841**

Serial No.: **10/612,990**

Examiner: **Jeremy C. NORRIS**

Filed: **July 7, 2003**

P.T.O. Confirmation No.: **1625**

For: **MULTILAYER CIRCUIT BOARD AND METHOD OF MANUFACTURING  
THE SAME**

*Enter w/pce  
dated  
7-9-07*

**RESPONSE UNDER 37 CFR §1.116**

**- EXPEDITED RESPONSE -  
GROUP ART UNIT 2841**

**MAILSTOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

May 27, 2007

Sir:

In response to the Office Action dated November 28, 2006, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 3 of this paper.